SUB SYSTEMS: SIMULTANEOUS SAMPLING, MULTIPLE TRACK HOLD

																							gnator		
	# Bits	11 0		Input	Conversion	Accuracy		Differential Fu		Full S		Zero		Bipolar						Temperature					
MODEL			Requirements		Voltage	Rate	or Linearity		Linearity E		Err		Error Lsb's		Zero Error Lsb's		7	oltage	Tristate	I/O	Range			#	Startin
		# CH	Min	+Icc		KSPS	Lsb's		Lsb's		Lsb's						Reference		Output		0	-25	-40 -55		Price
			+Vcc	mA			+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax	+25C	Tmax	INT	EXT	Latches		70C	85	85 125	Pins	/100's
AD7777, \	With I	Dual '	 Track a	and Ho	ld																				
AD7777	10	4	5	10/1	Vb-Vsw	380	1	1	1	1	12	12	12	12			+2			P10	J		A	24	\$9.75
AD7778, \	With I	Dual '	_ Track a	nd Ho	ld																				
AD7778	10	8	5	10/1	Vb-Vsw	380	1	1	1	1	12	12	12	12			+2			P10	J		A	24	\$10.70
AD7861 &	& AD7	874 (On Boa	urd 47	Track and	Hold Ampl	ifiers	, 4x12 (⊥ Outpi	ıt Reg	ister)														
AD7861	11	4	+5V	10	2.5V	200	2	2	2.5	2.5	13	13	9	9			+2.5V			P11			A	44	\$11.76
AD7874	12	4	±5	18/12	±10	30	1	1	1	1	5	5	NS	NS	5	5	+3V			P12		Α		28	\$30.80
AD7874	12	4					1/2	1/2														В	T		\$38.50
AD7864	14	4	+5V	18	0>2.5 ±2.5,±10V	285	1	1	1	1	4	4	3	3	4	4	+2.5V			P11			A	44	\$16.75
AD7864	14	4			±2.3,±10 ·		1/2	1/2			3	3			3	3									
AD7862 &	& AD7	863 I	Duals																						
AD7862	12	2	+5V	10	2.5V	200	1	1	1	1	4	4	4	4	4	4	+2.5V			P12			A	44	\$12.95
AD7862	12	2									3	3	4	3	3	3							В		\$16.85
AD7863	14	2	+5V	15	2.5V	200	2	2	1	1	4	4	4	4	4	4	+2.5V			P14			A	44	\$18.00
AD7863	14	2									3	3	4	3	3	3							В		tbd
Motion Co	ontrol	Co-F	rocess	or, w/4	T/H, 12 F	Bit Timer, P	WM.	Vector	Trai	sform	Block														
ADMC200	11	4	+5V	20	+5V	200	±2	±2	±2	±2	±6	±6	±5	±5			+2.5V			P11			A	44	\$24.64
ADMC20	1 CAR	AE A	e The	A DM	C200 PIJ	T WITH 6 D	ICIT	AT I/C) Dow	to													-		
		VIE A	_			_	_		_	_		1.0	1.5				. 2.537			D11				1.1	\$22.05
ADMC201	11	/	+5V	20	+5V	200	±2	±2	±2	±2	±6	±6	±5	±5			+2.5V			P11			A	44	\$33.95

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